

11002 U.S. PTO  
02/29/02

Best Available Copy

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10086129	FILING DATE 02/28/2002	CLASS 714	SUBCLASS 733	GAU 2133	EXAMINER <i>Decady</i>
**APPLICANTS: Schultz David; <i>RSD</i> <i>KERNEROS</i>					
**CONTINUING DATA VERIFIED: <i>None</i> <i>6/19/2004</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>None</i> <i>6/19/2004</i>					
PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO X-1069 US	
Verified and Acknowledged Examiners's initials <i>[Signature]</i>					
TITLE : Method and system for flexible nesting JTAG TAP controllers for FPGA-based system-on-chip (SoC)					
U.S. DEPT. OF COMM /PAT. & TM-PTO-436L(Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
		Sheets Drwg.	Figs.Drwg.
		Print Fig.	
ISSUE FEE			
Amount Due	Date Paid		
		Primary Examiner	
		Application Examiner	
TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

DISK (CRF)

CD-ROM  
(Attached in pocket on right inside flap)